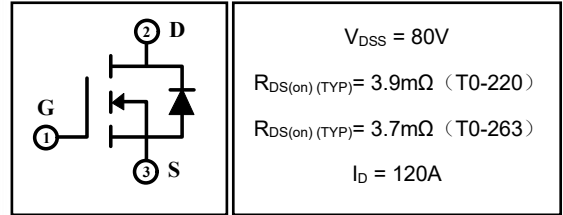


## 120A 80V N-channel Enhancement Mode Power MOSFET

### 1 Description

This N-channel enhancement mode power mosfets used advanced splite gate trench technology design, provided excellent R<sub>DS(on)</sub> and low gate charge. Which accords with the RoHS standard.

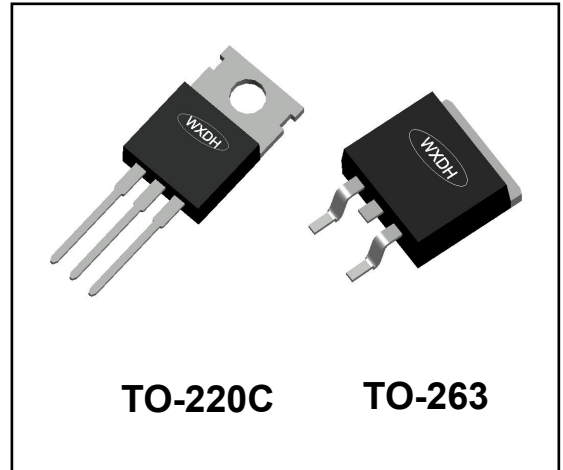


### 2 Features

- Fast switching
- Low on resistance
- Low gate charge
- High avalanche current
- Low reverse transfer capacitances
- 100% single pulse avalanche energy test
- 100%  $\Delta V_{DS}$  test

### 3 Applications

- Synchronous rectification in SMPS
- Hard switching and high speed circuit
- Power tools
- UPS
- Motor control



### 4 Electrical Characteristics

#### 4.1 Absolute Maximum Ratings (T<sub>c</sub>=25°C, unless otherwise noted)

Parameter		Symbol	Rating	Units
Drian-to-Source Voltage		$V_{DSS}$	80	V
Gate-to-Source Voltage		$V_{GSS}$	±20	V
Continuous Drain Current	T <sub>c</sub> =25°C (Package limit)	$I_D$	120	A
	T <sub>c</sub> =25°C (Silicon limit)		140	A
	T <sub>c</sub> =100°C		85	A
Pulsed Drain Current <sup>(1)</sup>		$I_{DM}$	480	A
Single Pulse Avalanche Energy <sup>(4)</sup>		$E_{AS}$	702	mJ
Avalanche Current <sup>(4)</sup>		$I_{AS}$	53	A
Power Dissipation	T <sub>a</sub> =25°C	$P_{tot}$	2	W
	T <sub>c</sub> =25°C	$P_{tot}$	208	W
Junction Temperature Range		$T_j$	-55~150	°C
Storage Temperature Range		$T_{stg}$	-55~150	°C

#### 4.2 Thermal Characteristics

Parameter	Symbol	Value		Units
		Typ	Max	
Thermal Resistance, Junction to Case-sink	$R_{thJC}$	0.6	0.78	°C/W
Thermal Resistance, Junction to Ambient	$R_{thJA}$	58	75	°C/W

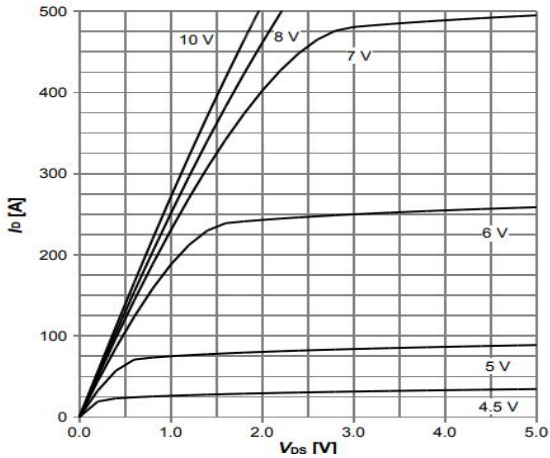
**4.3 Electrical Characteristics** (T<sub>c</sub>=25°C, unless otherwise noted)

Parameter	Symbol	Test Condition	Value			Units
			Min	Typ	Max	
<b>Off Characteristics</b>						
Drain-to-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V	80	--	--	V
Drain-to-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>C</sub> =25°C	--	--	1	μA
		V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>C</sub> =125°C	--	--	100	μA
Gate-to-Source Leakage	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
<b>On Characteristics</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2	3	4	V
Drain-to-Source on-state Resistance	R <sub>DS(on)</sub> (TO-220)	V <sub>GS</sub> =10V, I <sub>D</sub> =60A	--	3.9	4.5	mΩ
	R <sub>DS(on)</sub> (TO-263)	V <sub>GS</sub> =10V, I <sub>D</sub> =60A	--	3.7	4.3	mΩ
Transconductance	G <sub>fs</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =60A	--	130	--	S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =42.5V, f=1.0MHz	--	3270	--	pF
Output Capacitance	C <sub>oss</sub>		--	705	--	
Reverse Transfer Capacitance	C <sub>rss</sub>		--	38	--	
Gate Resisitance	R <sub>G</sub>	V <sub>DD</sub> =0V, V <sub>GS</sub> =0V, F=1 MHz	--	1.6	--	Ω
<b>Switching Characteristics</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	I <sub>D</sub> =60A, V <sub>DD</sub> =30V, V <sub>GS</sub> =10V, R <sub>GEN</sub> =4.7Ω	--	16	--	nS
Turn-on Rise Time	t <sub>r</sub>		--	29	--	
Turn-off Delay Time	t <sub>d(off)</sub>		--	42	--	
Turn-off Fall Time	t <sub>f</sub>		--	30	--	
Total Gate Charge	Q <sub>g</sub>	I <sub>D</sub> =60A, V <sub>DD</sub> =45V, V <sub>GS</sub> =10V	--	51	--	nC
Gate-to-Source Charg	Q <sub>gs</sub>		--	17	--	
Gate-to-Drain("Miller") Charge	Q <sub>gd</sub>		--	13	--	
Gate plateau voltage	V <sub>plateau</sub>		--	5.3	--	V
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltag	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =60A	--	--	1.3	V
Diode Forward Curren	I <sub>S</sub>		--	--	120	A
Reverse Recovery	t <sub>rr</sub>	T <sub>J</sub> =25°C, I <sub>F</sub> =50A, dl <sub>F</sub> /dt=100A/μS, V <sub>GS</sub> =0V	--	53	--	nS
Reverse Recovery Charge <sup>(3)</sup>	Q <sub>rr</sub>		--	72	--	nC

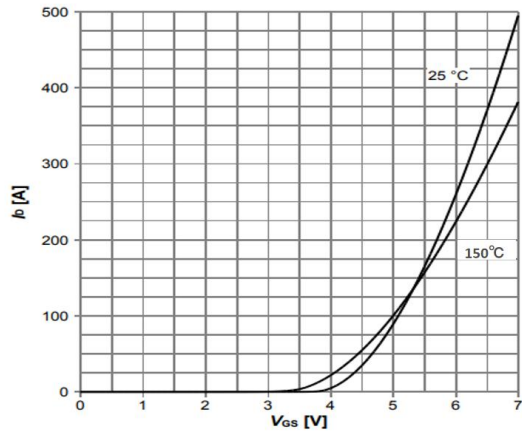
**Notes:**

- 1: Repetitive rating, pulse width limited by maximum junction temperature.
- 2: Surface mounted on FR4 Board, t<sub>s</sub>≤10sec.
- 3: Pulse width ≤ 300μs, duty cycle ≤ 2%.
- 4: L=0.5mH, V<sub>DD</sub>=80V, V<sub>GATE</sub>=85V, Start T<sub>J</sub>=25°C.

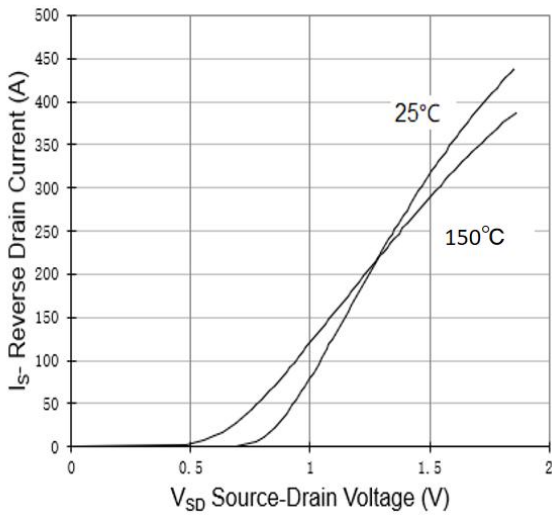
**5 Typical characteristics diagrams**



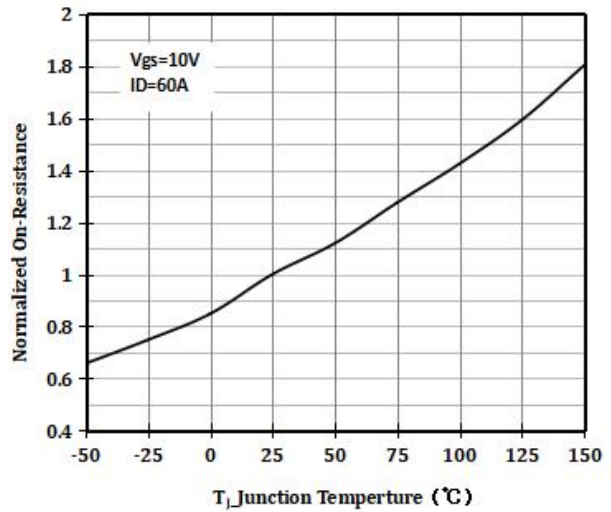
**Fig 1. Output Characteristics**



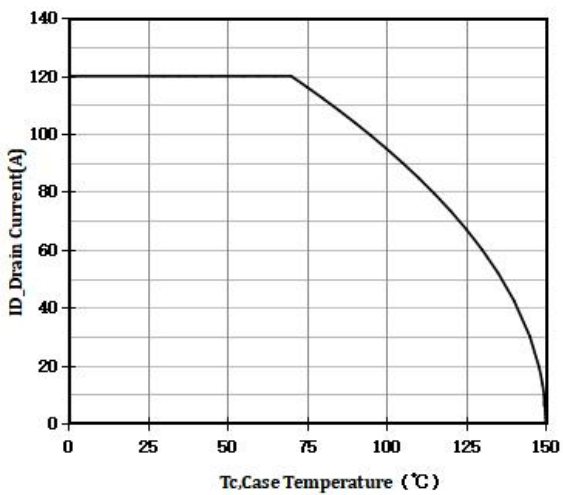
**Fig 2. Transfer Characteristics**



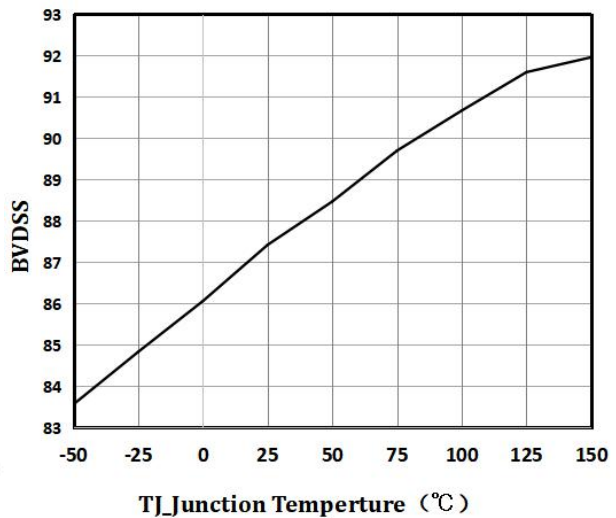
**Figure 3 Source-Drain Diode Forward**



**Fig 4. Rds(on) vs. Temperature**

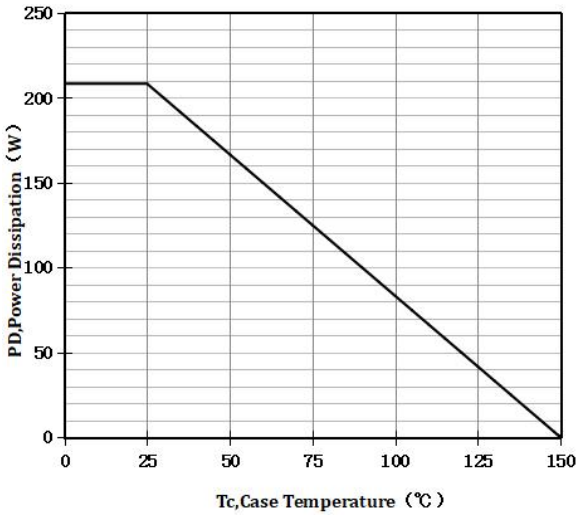


**Figure 5. ID Current Derating**

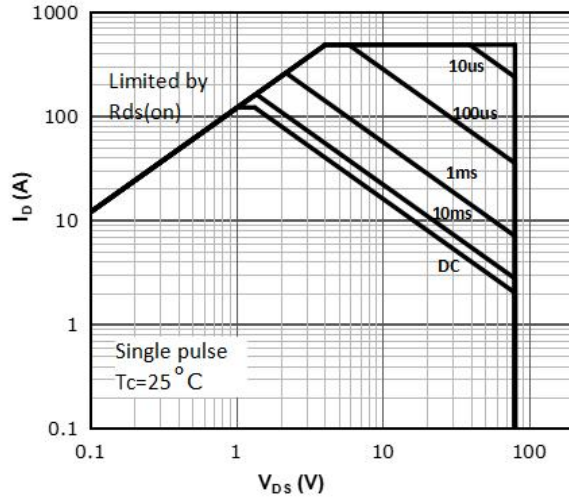


**Figure 6. BVDSS vs. Temperature**

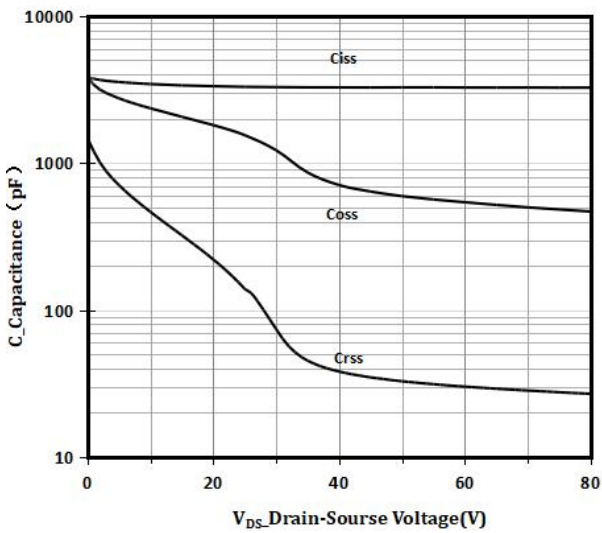
**5 Typical characteristics diagrams(continues)**



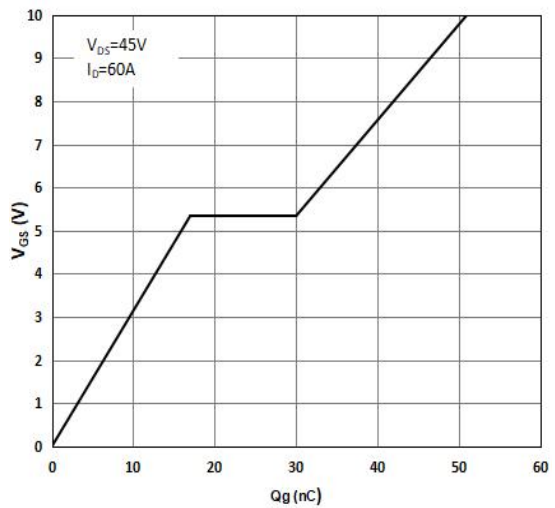
**Figure 7. Power Dissipation**



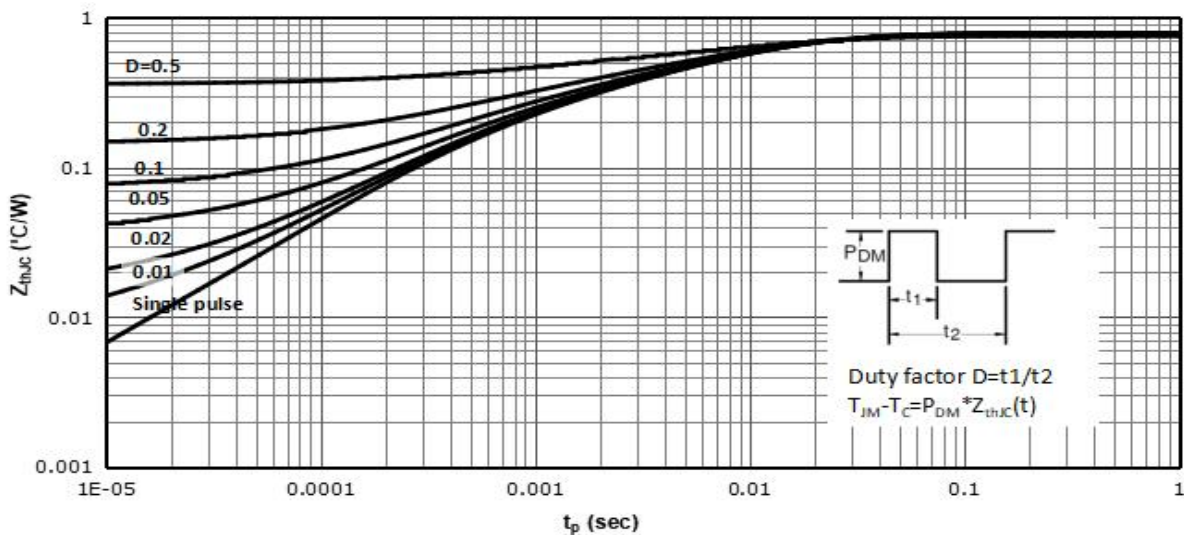
**Figure 8. Safe Operating Area**



**Figure 9. Capacitance Characteristics**

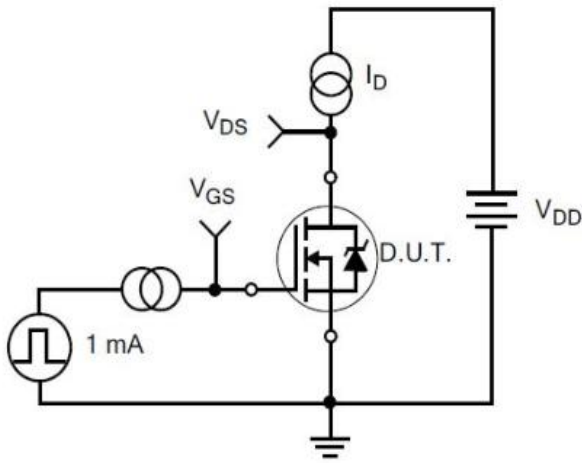


**Figure 10. Gate Charge Characteristics**

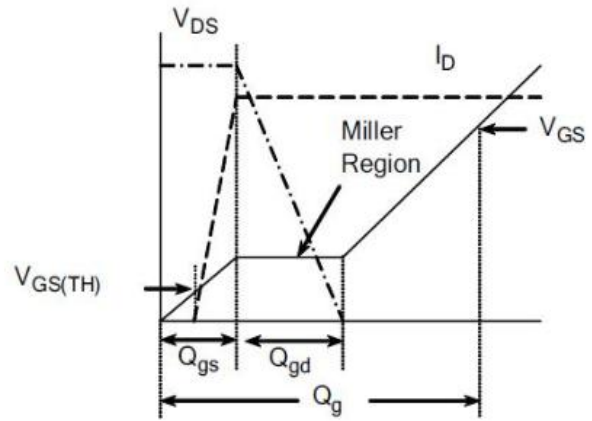


**Figure 11. Normalized Maximum Transient Thermal Impedance**

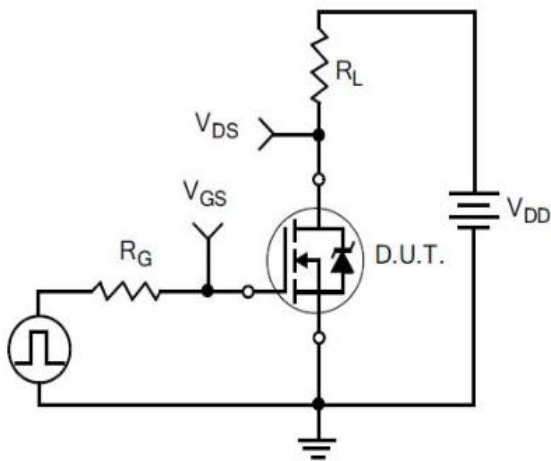
**6 Typical Test Circuit and Waveform**



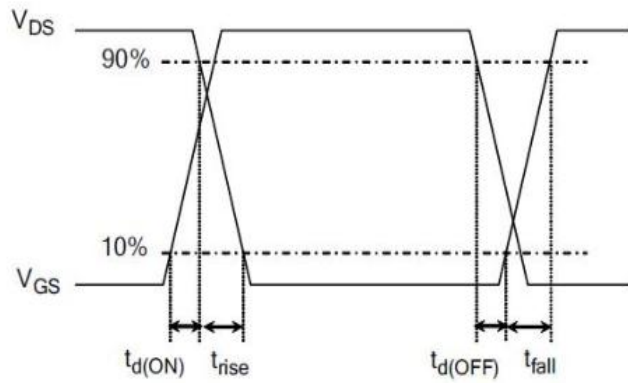
1) Gate Charge Test Circuit



2) . Gate Charge Waveform

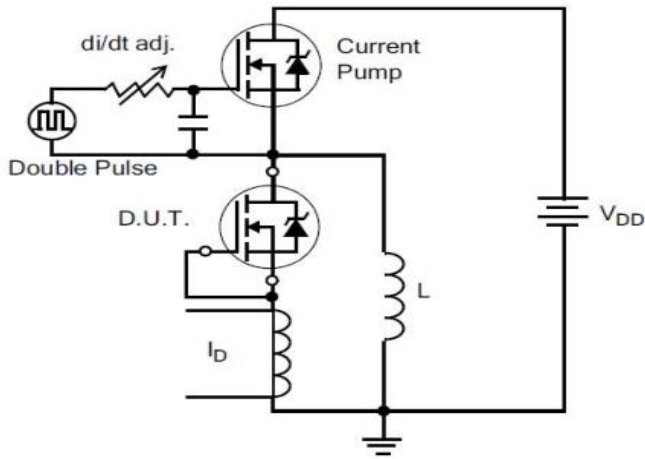


3) Resistive Switching Test Circuit

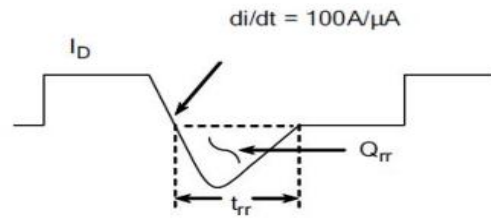


4) Resistive Switching Waveforms

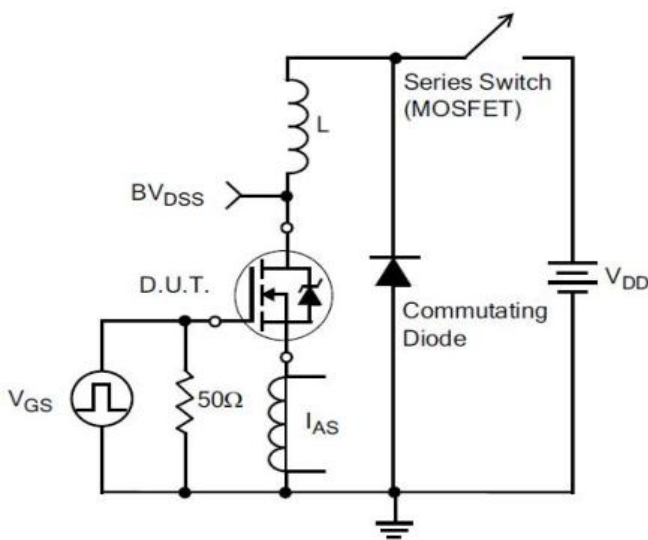
**6 Typical Test Circuit and Waveform(continues)**



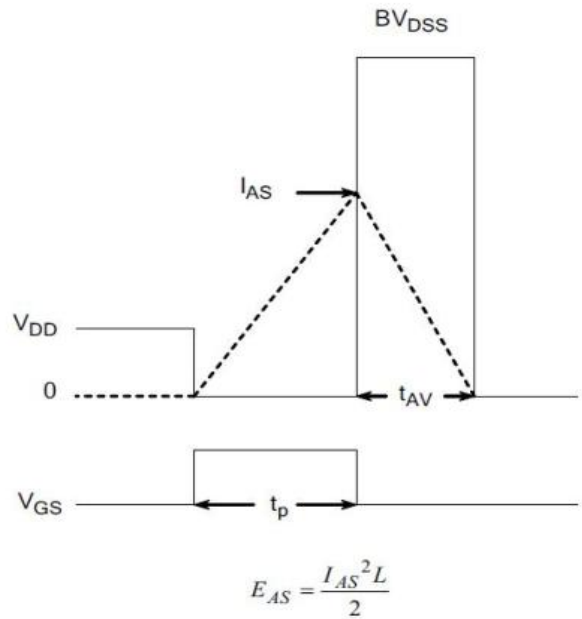
5) Diode Reverse Recovery Test Circuit



6) Diode Reverse Recovery Waveform



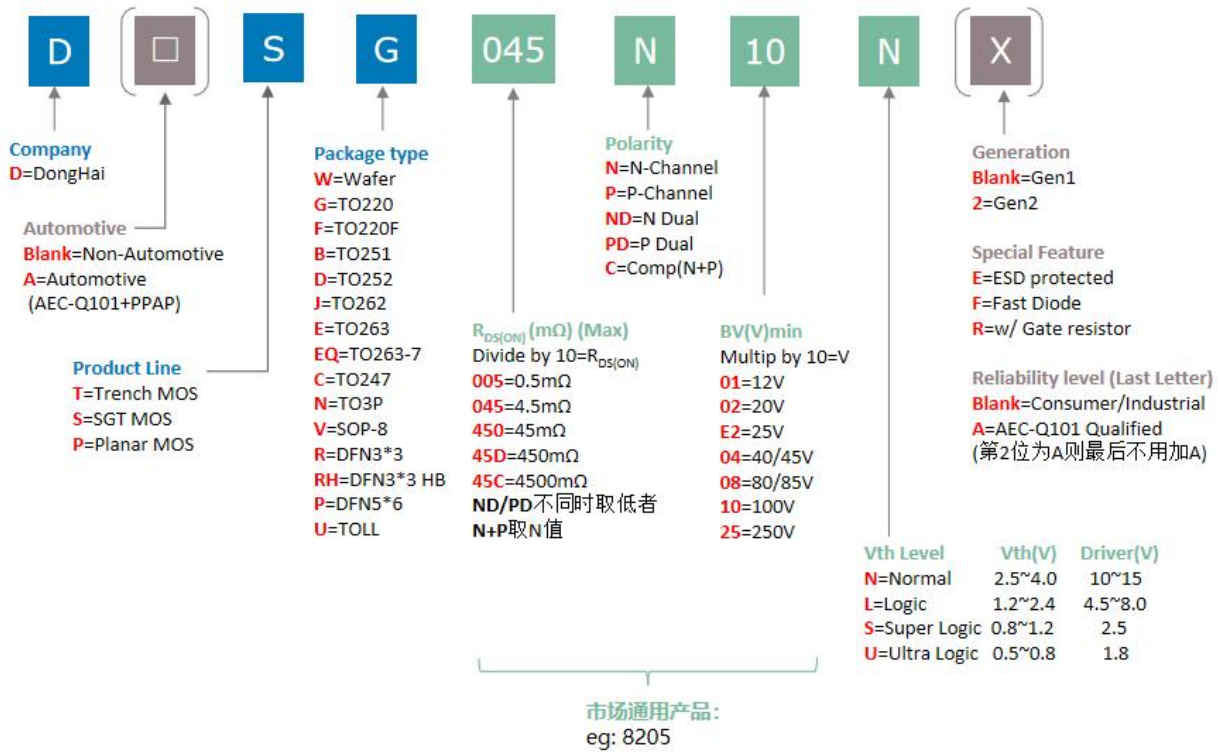
7) . Unclamped Inductive Switching Test Circuit



8) Unclamped Inductive Switching Waveforms



## 7 Product Names Rules

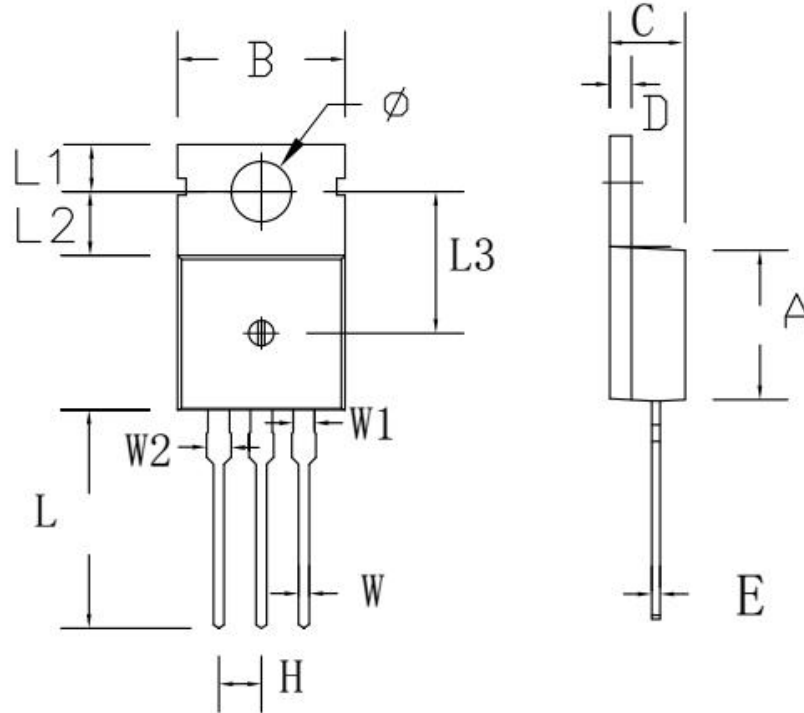


## 8 Product Specifications and Packaging Models

Product Model	Package Type	Mark Name	RoHS	Package	Quantity
DSG047N08N3	TO-220	DSG047N08N3	Pb-free	Tube	1000/box
DSE047N08N3	TO-263	DSE047N08N3	Pb-free	Tube	800/box

9 Dimensions

TO-220C PACKAGE OUTLINE DIMENSIONS

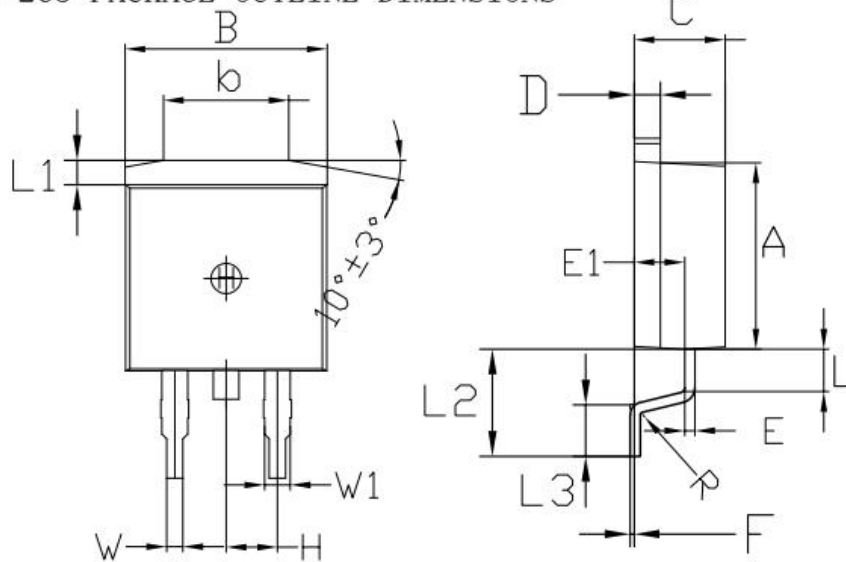


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	min.	max.	min.	max.
A	8.80	9.30	0.346	0.366
B	9.70	10.30	0.382	0.406
C	4.25	4.75	0.167	0.187
D	1.20	1.45	0.047	0.057
E	0.40	0.60	0.016	0.024
H	2.54 TYP		0.100 TYP	
W	0.60	0.95	0.024	0.037
W1	1.05	1.45	0.041	0.057
W2	1.20	1.60	0.047	0.063
L	12.60	13.40	0.496	0.528
L1	2.45	2.95	0.096	0.116
L2	3.45	3.95	0.136	0.156
L3	8.15	8.65	0.321	0.341
Φ	3.50	3.90	0.138	0.154



9 Dimensions(continues)

TO-263 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	min.	max.	min.	max.
A	8.80	9.30	0.346	0.366
B	9.70	10.30	0.382	0.406
C	4.25	4.75	0.167	0.187
D	1.20	1.45	0.047	0.057
E	0.40	0.60	0.016	0.024
L	1.90	2.30	0.075	0.091
L1	1.15	1.45	0.045	0.057
R	0.24	0.26	0.0095	0.0102
W	0.80	0.82	0.0315	0.0323
W1	1.20	1.30	0.047	0.051
H	2.54 TYP		0.200 TYP	
b	5.50	6.50	0.216	0.256
E1	2.4	2.6	0.0946	0.1024
L2	5.20	5.80	0.205	0.228
L3	2.20	3.20	0.087	0.126
F	0.03	0.23	0.0012	0.0091

## 10 Attentions

- Jiangsu Donghai Semiconductor Technology CO.,LTD. reserves the right to change the specification without prior notice! The customer should obtain the latest version of the information before making the order and verify that the information is complete and up to date.
- It is the responsibility of the purchaser for any failure or failure of any semiconductor product under certain conditions. It is the responsibility of the purchaser to comply with safety standards and to take safety measures in the system design and machine manufacturing of Donghai products in order to avoid potential risk of failure. Injury or property damage.
- Product promotion is endless, our company will be dedicated to provide customers with better products.

## 11 Appendix

Revision history:

Date	REV.	Description	Page
2022.11.07	1.0	Original	10